

WHAT IS CLAIMED IS:

1. . A liquid crystal display device comprising:

first and second substrates that are divided in a display area and a non-display area;

a plurality of switching devices on the first substrate;

5 first and second lines that apply signals to each switching device;

a plurality of first electrodes on the first substrate;

a first auxiliary line arranged in the non-display area, the first auxiliary line
receiving a first signal;

a second auxiliary line arranged in the non-display area, the second auxiliary line
10 receiving a second signal; and

a liquid crystal layer between the first and second substrates.
2. The device of claim 1, wherein the switching device includes a thin film transistor.
- 15 3. The device of claim 1, wherein the first line includes a gate line.

4. The device of claim 1, wherein the second line includes a data line.
5. The device of claim 1, wherein the first electrodes include a pixel electrode.
- 5 6. The device of claim 1, further comprising second electrodes on the first substrate wherein the second electrodes include a common electrode.
7. The device of claim 6, wherein the plurality of the second electrodes receives the first signal.
- 10 8. The device of claim 1, wherein the first line receives the second signal.
9. The device of claim 1, wherein the first auxiliary line is parallel with the first line.
- 15 10. The device of claim 1, wherein the second auxiliary line is parallel with the first line.

11. The device of claim 1, wherein the first auxiliary line includes a common line.

12. The device of claim 1, wherein the second auxiliary line receives a signal having polarity opposite to a polarity of a signal applied to the first line.

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13. The device of claim 1, further comprising:

a plurality of pad portions in the non-display area; and

a plurality of electrostatic discharge device in the non-display area.

10 14. An array substrate for a liquid crystal display device, comprising:

a substrate having a display area and a non-display area;

a plurality of switching devices arranged in the display area of the substrate;

first and second lines that apply signals to each switching device;

a plurality of first electrodes on the substrate;

15 a first auxiliary line arranged in the non-display area, the first auxiliary line receiving a first signal; and

a second auxiliary line arranged in the non-display area, the second auxiliary line receiving a second signal.

15. The array substrate of claim 14, wherein the second auxiliary line receives a same
5 signal as the first auxiliary line.

16. The array substrate of claim 14, wherein the first line is a gate line.

17. The array substrate of claim 14, wherein the second auxiliary line receives a signal
10 having a polarity periodically opposite to a polarity of a signal received by the first
auxiliary line.

18. The arrays substrate of claim 14, further comprising a plurality of second
electrodes, wherein the plurality of the second electrodes receive the first signal.

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19. The array substrate of claim 14, wherein the first line receives the second signal.